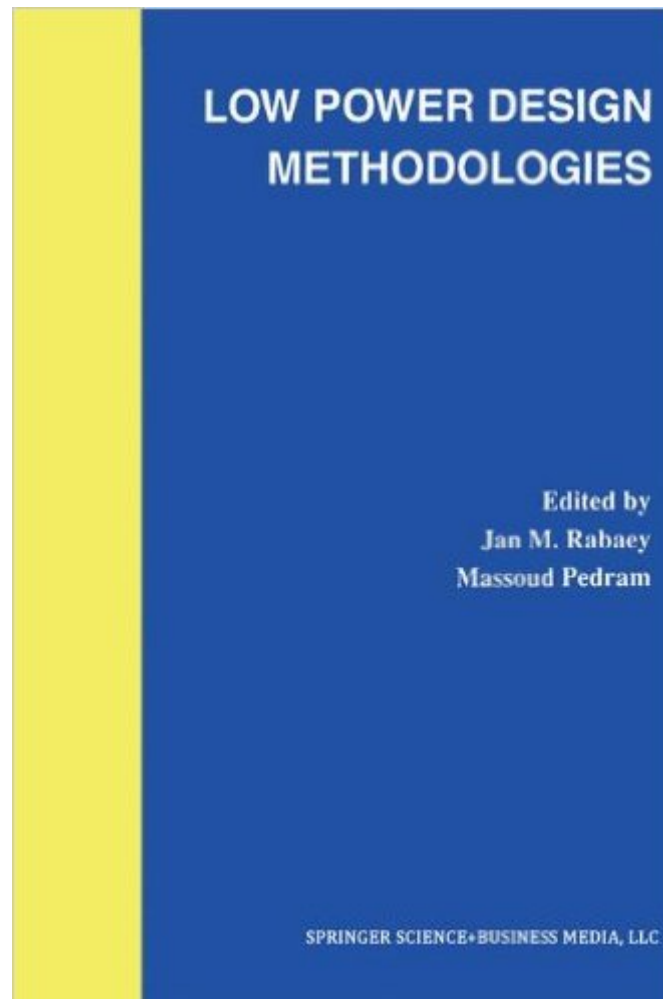# Low Power Design Methodologies (The Springer International Series In Engineering And Computer Science)

LOW POWER DESIGN METHODOLOGIES

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SPRINGER SCIENCE+BUSINESS MEDIA, LLC

Synopsis

Low Power Design Methodologies presents the first in-depth coverage of all the layers of the design hierarchy, ranging from the technology, circuit, logic and architectural levels, up to the system layer. The book gives insight into the mechanisms of power dissipation in digital circuits and presents state of the art approaches to power reduction. Finally, it introduces a global view of low power design methodologies and how these are being captured in the latest design automation environments. The individual chapters are written by the leading researchers in the area, drawn from both industry and academia. Extensive references are included at the end of each chapter. Audience: A broad introduction for anyone interested in low power design. Can also be used as a text book for an advanced graduate class. A starting point for any aspiring researcher.

Book Information

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This is an excellent book for people who have already taken atleast one course in CMOS Design or as a reference for people in the industry. A good background in understanding of CMOS circuits operation is a must, which generally will be the case for anyone who is considering this book. An excellent book for second year Grad students who have sufficient background and want to go deep into the modern VLSI Chip design methodologies and research.

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